

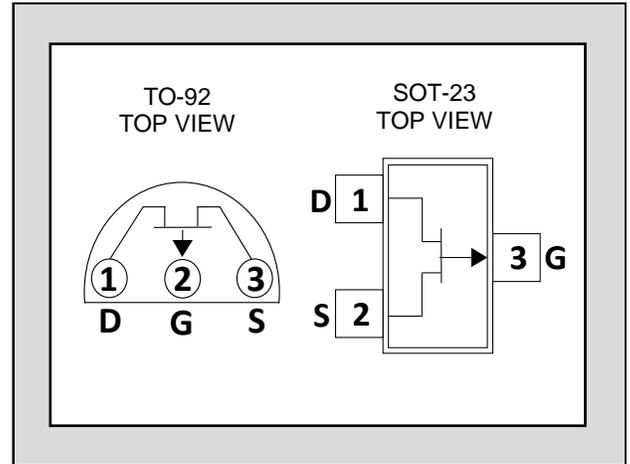
# LINEAR SYSTEMS

Over 30 Years of Quality Through Innovation

## LSJ289

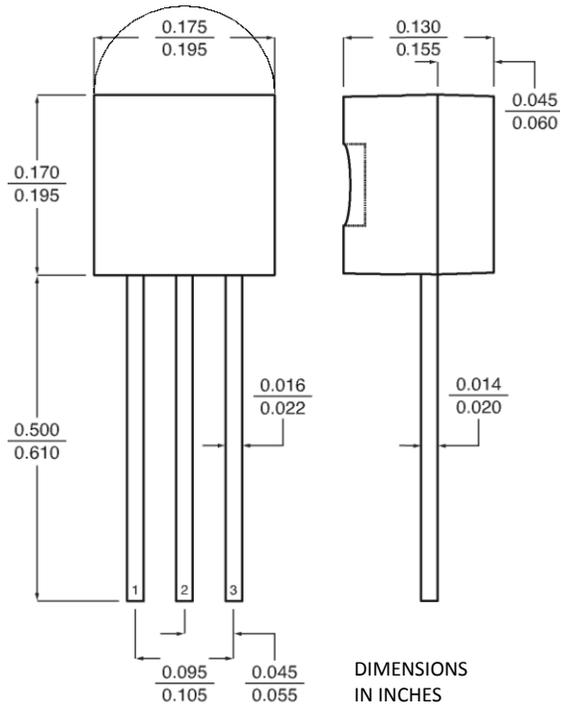
LOW NOISE, LOW CAPACITANCE  
SINGLE P-CHANNEL JFET

FEATURES	
ULTRA LOW NOISE	$e_n = 2.0\text{nV}/\sqrt{\text{Hz}}$
LOW INPUT CAPACITANCE	$C_{ISS} = 8\text{pF}$
<b>ABSOLUTE MAXIMUM RATINGS<sup>1</sup></b> @ 25 °C (unless otherwise stated)	
<b>Maximum Temperatures</b>	
Storage Temperature	-55 to +150°C
Junction Operating Temperature	-55 to +150°C
<b>Maximum Power Dissipation</b>	
Continuous Power Dissipation TA=25°C	300mW <sup>4</sup>
<b>Maximum Currents</b>	
Gate Forward Current	$I_{G(F)} = 10\text{mA}$
<b>Maximum Voltages</b>	
Gate to Source	$V_{GSO} = 50\text{V}$
Gate to Drain	$V_{GDO} = 50\text{V}$

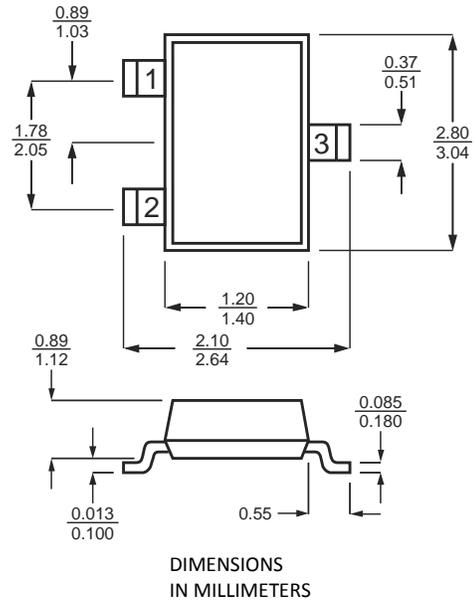


SYMBOL	CHARACTERISTIC	MIN	TYP	MAX	UNITS	CONDITIONS
$BV_{GSS}$	Gate to Source Breakdown Voltage	50			V	$V_{DS} = 0\text{V}, I_D = -1\ \mu\text{A}$
$V_{GS(OFF)}$	Gate to Source Pinch-off Voltage	1.5		5.0	V	$V_{DS} = -15\text{V}, I_D = -1\text{nA}$
$I_{DSS}^2$	Drain to Source Saturation Current	-2.5		-30	mA	$V_{DS} = -15\text{V}, V_{GS} = 0$
$I_G$	Gate Operating Current		2		pA	$V_{DG} = -15\text{V}, I_D = -200\ \mu\text{A}$
$I_{GSS}$	Gate to Source Leakage Current			100	pA	$V_{GS} = -15\text{V}, V_{DS} = 0\text{V}$
$G_{fs}$	Full Conductance Transconductance	1500			$\mu\text{S}$	$V_{DS} = -15\text{V}, V_{GS} = 0, f = 1\text{kHz}$
			1500		$\mu\text{S}$	$V_{DS} = -15\text{V}, I_D = -200\ \mu\text{A}, f = 1\text{kHz}$
$G_{OS}$	Full Output Conductance		38		$\mu\text{S}$	$V_{DS} = -15\text{V}, V_{GS} = 0, f = 1\text{kHz}$
$G_{OS}$	Output Conductance		3		$\mu\text{S}$	$V_{DS} = -15\text{V}, I_D = -200\ \mu\text{A}, f = 1\text{kHz}$
NF	Noise Figure		0.5		dB	$V_{DS} = -15\text{V}, V_{GS} = 0, R_G = 10\text{M}\Omega, f = 100\text{Hz}$
$e_n$	Noise Voltage		2.0		$\text{nV}/\sqrt{\text{Hz}}$	$V_{DS} = -15\text{V}, I_D = -2\text{mA}, f = 1\text{kHz},$
$e_n$	Noise Voltage		3.5		$\text{nV}/\sqrt{\text{Hz}}$	$V_{DS} = -15\text{V}, I_D = -2\text{mA}, f = 10\text{Hz},$
$C_{ISS}$	Common Source Input Capacitance		8		pF	$V_{DS} = -15\text{V}, I_D = -500\ \mu\text{A}, f = 1\text{MHz}$
$C_{RSS}$	Common Source Reverse Transfer Cap.		3		pF	

## TO-92



## SOT-23

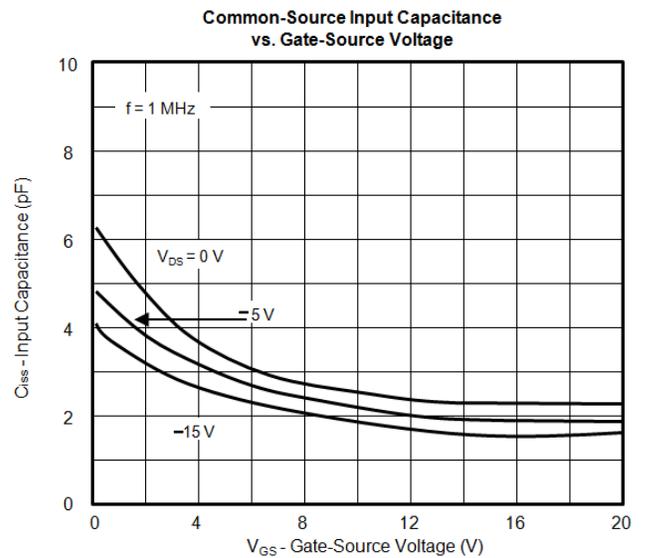
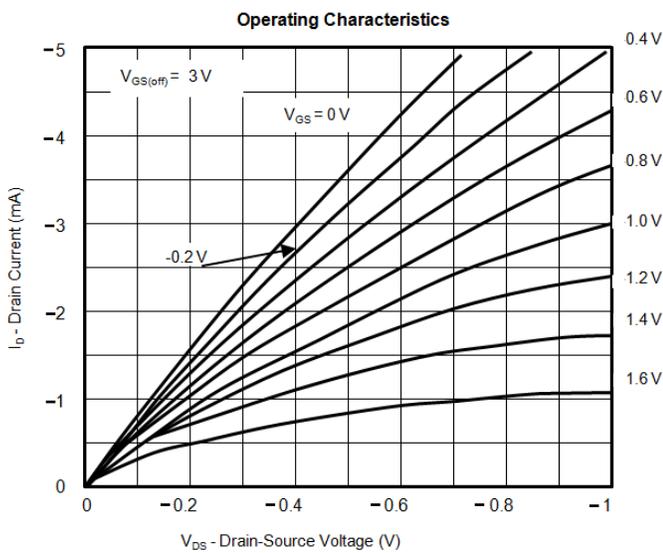
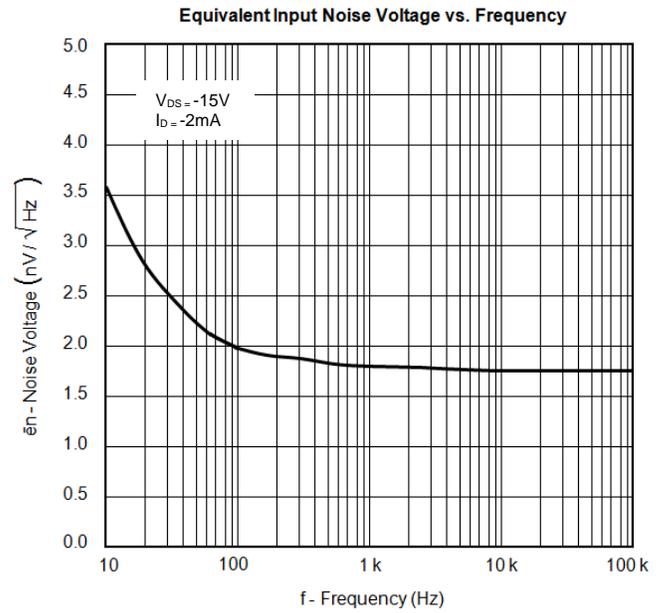
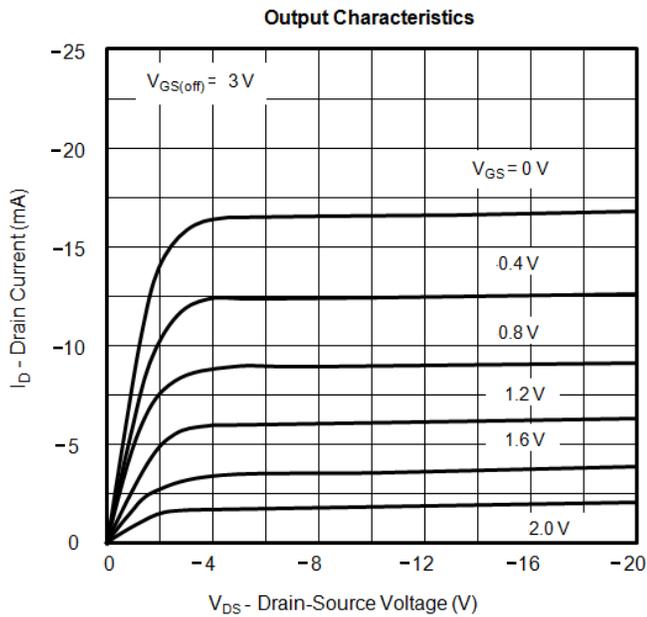
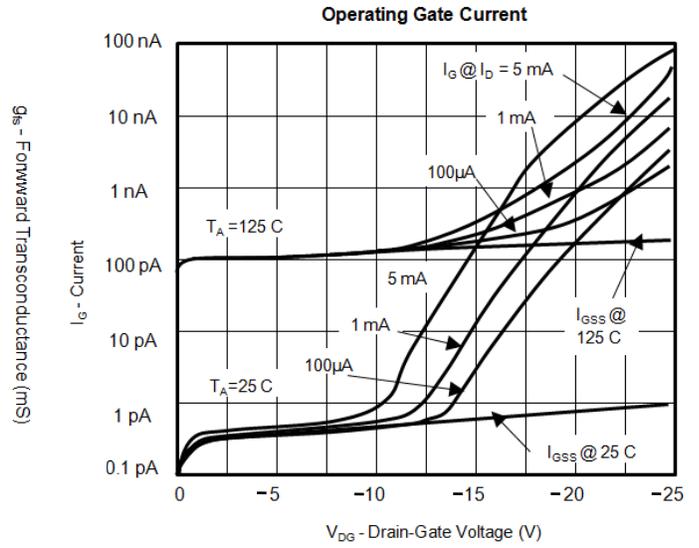
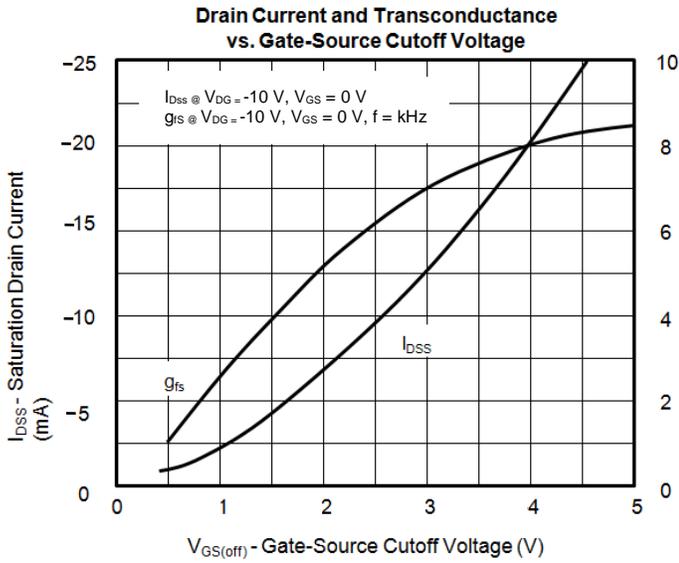


### NOTES:

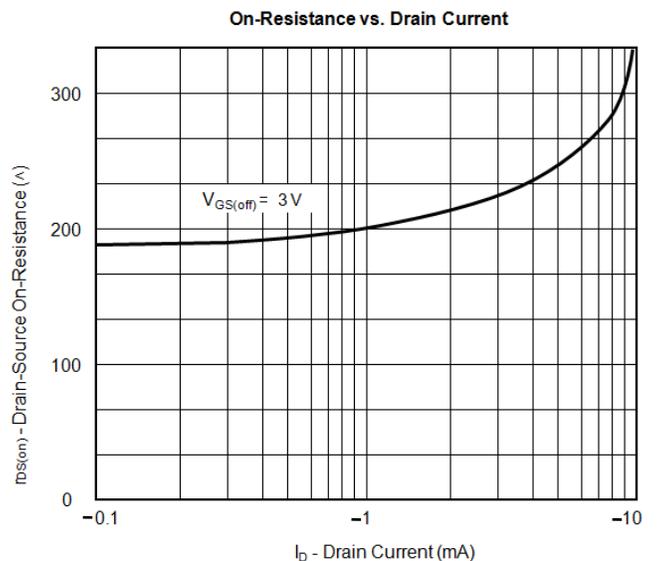
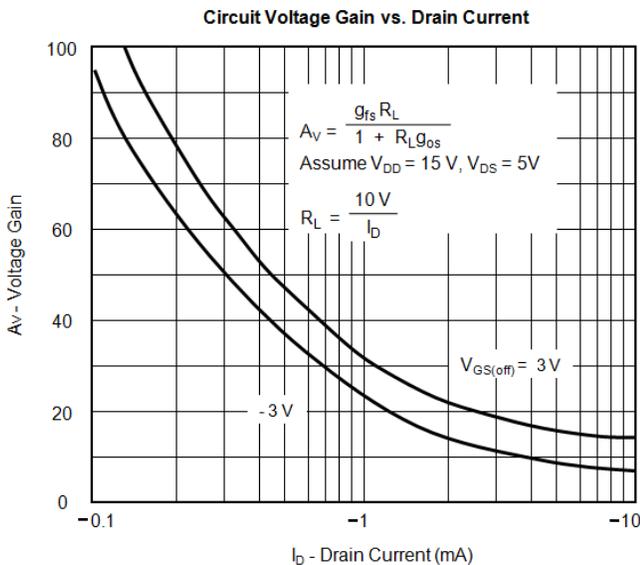
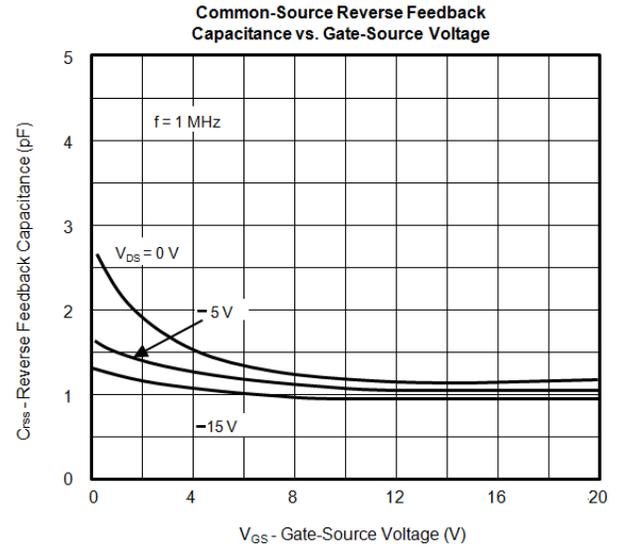
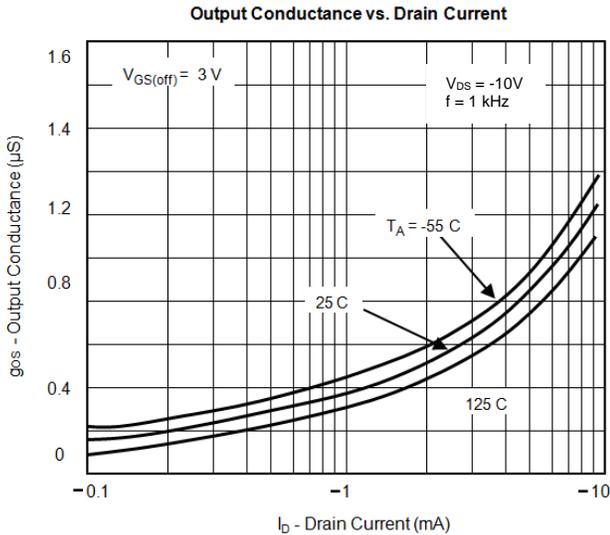
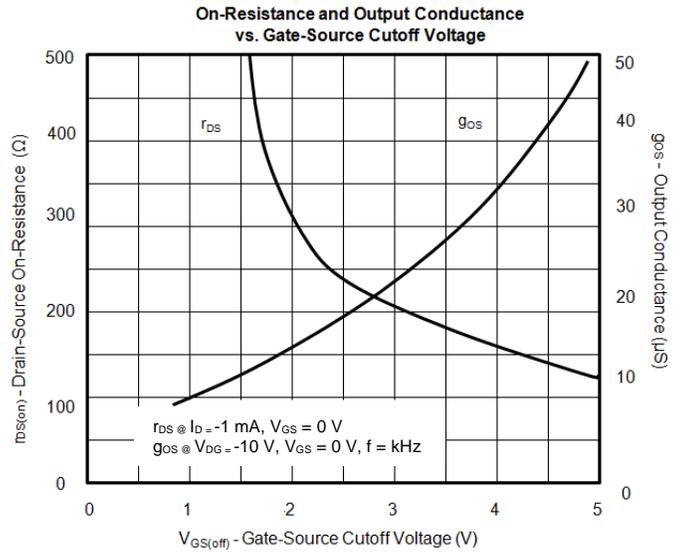
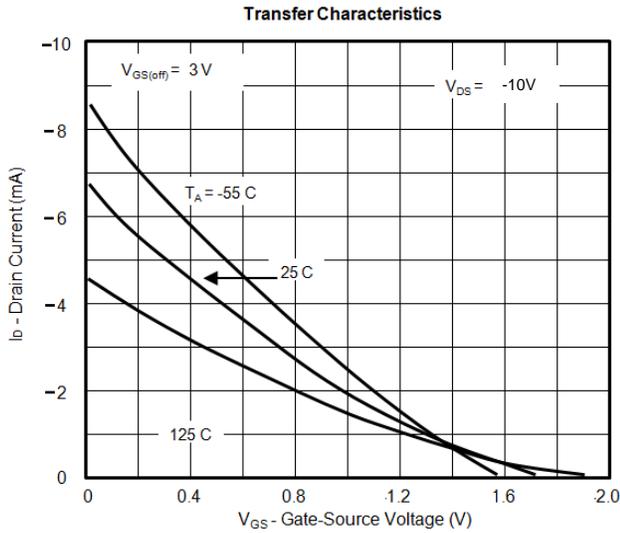
1. Absolute maximum ratings are limiting values above which serviceability may be impaired.
2. Pulse Test:  $PW \leq 300\mu s$ , Duty Cycle  $\leq 3\%$ .
3. All characteristics MIN/TYP/MAX numbers are absolute values. Negative values indicate electrical polarity only.
4. Derate 2.8 mW  $^{\circ}C$  above 25 $^{\circ}C$ .

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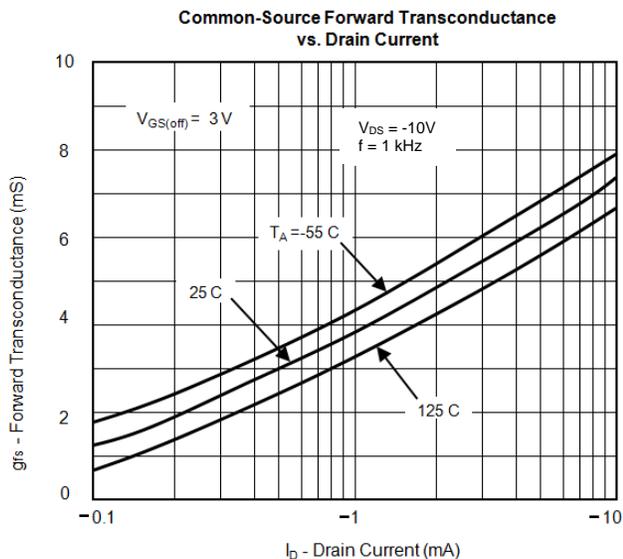
# Typical Characteristics



# Typical Characteristics (Cont'd)



## Typical Characteristics (Cont'd)



Linear Integrated Systems develops and produces the highest performance semiconductors of their kind in the industry. Linear Systems, founded in 1987, uses patented and proprietary processes and designs to create its high performance discrete semiconductors. Expertise brought to the company is based on processes and products developed at Amelco, Union Carbide, Intersil and Micro Power Systems by company founder John H. Hall.