# LINEAR SYSTEMS

# 3N163 and 3N164

Over 30 Years of Quality Through Innovation

## P-Channel Enhancement Mode MOSFET

## VERY HIGH INPUT IMPEDANCE, HIGH GATE BREAKDOWN, FAST SWITCHING, LOW CAPACITANCE

FEATURES					
VERY HIGH INPUT IMPEDANCE					
HIGH GATE BREAKDOWN					
ULTRA LOW LEAKAGE					
FAST SWITCHING					
LOW CAPACITANCE					
ABSOLUTE MAXIMUM RATINGS					
@ 25°C (unless otherwise stated)					
Drain-Source or Drain-Gate Voltage					
3N163	-40V				
3N164	-30V				
Drain Current	50mA				
Storage Temperature	-55°C to +150°C				
Power Dissipation TO-72 case	375mW²				
Power Dissipation SOT-143 case	350mW <sup>3</sup>				







TO-72 4L PACKAGE PHOTO



SOT-143 4L TOP VIEW



#### ELECTRICAL CHARACTERISTICS @ 25°C (unless otherwise noted)

SYMBOL	CHARACT	ERISTIC	3N163		3N164		UNITS	CONDITIONS	
			MIN	MAX	MIN	MAX			
Igss	Gate Leakage Current			-10		-10		$V_{GS}$ =-40V,	V <sub>DS</sub> =0 (3N163), V <sub>SB</sub> =0V
		T <sub>A</sub> =+125°C		-25		-25	pА	$V_{GS}=-30V$ ,	V <sub>DS</sub> =0 (3N164), V <sub>SB</sub> =0V
BV <sub>DSS</sub>	Drain-Source Breakd	lown Voltage	-40		-30			I <sub>D</sub> =-10μΑ	$V_{GS}=0$ , $V_{BS}=0$
BV <sub>SDS</sub>	Source-Drain Breakdown Voltage		-40		-30			Is=-10µA	$V_{GD}=0, V_{BD}=0$
VGS(th)	Threshold Voltage		-2.0	-5.0	-2.0	-5.0	V	$V_{DS}=V_{GS}$	I <sub>D</sub> =-10μΑ, V <sub>SB</sub> =0V
V <sub>GS</sub>	Gate Source Voltage (on)		-3.0	-6.5	-3.0	-6.5		V <sub>DS</sub> =-15V	I <sub>D</sub> =-0.5mA, V <sub>SB</sub> =0V
IDSS	Zero Gate Voltage, Drain Current (off)			-200		-400	54	V <sub>DS</sub> =-15V	V <sub>GS</sub> =0, V <sub>SB</sub> =0V
I <sub>SDS</sub>	Zero Gate Voltage, Source Current			-400		-800	рА	$V_{SD}$ =-15V	$V_{GS}=0, V_{DB}=0V$
RDS(on)	Drain-Source on Resistance			250		300	ohms	V <sub>GS</sub> =-20V	I <sub>D</sub> =-100μΑ, V <sub>SB</sub> =0V
I <sub>D(on)</sub>	On Drain Current		-5.0	-30	-3.0	-30	mA	$V_{DS}$ =-15V	$V_{GS}$ =-10V, $V_{SB}$ =0V
<b>g</b> fs	Forward Transcondu	ctance	2.0	4.0	1.0	4.0	mS		I <sub>D</sub> =-10mA f=1kHz
<b>g</b> og	Output Admittance			250		250	μS	VDS=-15V	
Ciss	Input Capacitance-O	utput Shorted		3.5		3.5			
Crss	Reverse Transfer Ca	pacitance		0.7		0.7	pF	V <sub>DS</sub> =-15V	I <sub>D</sub> =-10mA <sup>1</sup> f=1MHz
Coss	Output Capacitance	Input Shorted		3.0		3.0			

## P-Channel Enhancement Mode MOSFET

SWITCHING CHARACTERISTICS TA=25°C and VBS=0 (unless otherwise noted)							
SYMBOL	CHARACTERISTIC	3N163		3N164		UNITS	CONDITIONS
		MIN	MAX	MIN	MAX		
t <sub>on</sub>	Turn-On Delay Time		12		12		VDD=-15V. VSB=0V
tr	Rise Time		24		24	ns	I <sub>D(on)</sub> =-10mA <sup>1</sup>
t <sub>off</sub>	Turn-Off Time		50		50		$R_G=R_L=1.4K$

#### SWITCHING CHARACTERISTICS T<sub>A</sub>=25°C and V<sub>BS</sub>=0 (unless otherwise noted)





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### NOTES:

- 1. For design reference only, not 100% tested.
- 2. Derate 3mW/°C above 25°C
- 3. Derate 3.5mW/ºC above 25ºC
- 4. All min/max limits are absolute numbers. Negative signs indicate electrical polarity only.

Stresses above those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these or any other conditions above those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

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