



Linear Integrated Systems

DPAD/SSTD PAD Series Ordering and Part Marking Information

5/19/09

SUBJECT: DPAD/SSTD PAD Series Ordering and Part Marking Information

The DPAD Series Monolithic Dual Low Leakage Diodes are offered in a TO-78, 5 lead package and includes the following part numbers: **DPAD1**

The DPAD Series Monolithic Dual Low Leakage Diodes are offered in a TO-72, 4 lead package and includes the following part numbers: **DPAD2, DPAD5, DPAD10, DPAD20, DPAD50, DPAD100**

The SSTD PAD Series Monolithic Dual Low Leakage Diodes are offered in a SOIC, 8 lead package and includes the following part numbers: **SSTD PAD5, SSTD PAD10, SSTD PAD20, SSTD PAD50, SSTD PAD100**

Ordering Information:

Parts are available with ROHS Compliant/Leadfree package versions as well as package versions that contain lead. Parts are available tested to the Standard Datasheet limits and conditions, as well and Special limits and conditions provided by the customer. When ordering your part please be aware of the 4 options you have:

- DPAD1 TO-78 5L ROHS**-----Electrically tested to Standard Datasheet Limits/Conditions, Leadfree/ROHS Compliant package requested.
- DPAD1 TO-78 5L-ROHS-Select**----Electrically tested to Special Customer Limits/Conditions, Leadfree/ROHS Compliant package requested.
- DPAD2 TO-72 4L ROHS**-----Electrically tested to Standard Datasheet Limits/Conditions, Leadfree/ROHS Compliant package requested.
- DPAD2 TO-72 4L-ROHS-Select**----Electrically tested to Special Customer Limits/Conditions, Leadfree/ROHS Compliant package requested
- SSTD PAD5 SOIC 8L**-----Electrically tested to Standard Datasheet Limits/Conditions, Lead or Leadfree/ROHS compliant package not specified.
- SSTD PAD5 SOIC 8L-Select**-----Electrically tested to Special Customer Limits/Conditions, Lead or Leadfree/ROHS Compliant package not specified.
- SSTD PAD5 SOIC 8L ROHS**-----Electrically tested to Standard Datasheet Limits/Conditions, Leadfree/ROHS Compliant package requested, marked with the letter “F”
- SSTD PAD5 SOIC 8L-ROHS-Select**-----Electrically tested to Special Customer Limits/Conditions, Leadfree/ROHS Compliant package requested., marked with the letter “F”

Important Part Marking Information:

In the event you are ordering multiple versions of the DPAD/SSTDPAD series it is very important that you **keep both groups of parts separate from each other and do not mix.** Each container/reel/bag of parts is labeled with the complete part number you have ordered. **The parts are NOT marked with the complete part number. The parts are marked with the SAME part number, and can be easily mixed with the wrong part number in the event proper inventory control procedures are not followed at customer level.**

As a result of our manufacturing flow, the entire wafer may contain more than one part number per wafer. The entire wafer is tested to the all of the DPAD/SSTDPAD limits. The wafer is sent out for assembly where the parts are marked prior to test. At test, the parts are separated into the individual part numbers then labeled and placed into inventory. Due to the space restrictions on the number of digits we can print on the two different package types, and our inability to remark the part after test limit our ability mark individual or complete part numbers.

Device Marking Prior to Date Code 0900:

Prior to date code 0900, there were different devices marking which may have been used. We now are standardizing our part marking avoid confusion at the customer level. All of the part marking that is listed in this document represents how the devices will be marked in the future. Any different marking represents old/past part marking which has been replace with the part marking as listed below.

Vishay/Siliconix Part Number	Package	LIS Part Number	Part Marking
DPAD1	TO-78 5L	DPAD1 TO-78 5L RoHS DPAD1 TO-78 5L RoHS SELECT	LS DPAD LS DPAD
DPAD2	TO-72 4L	DPAD2 TO-72 4L RoHS DPAD2 TO-72 4L RoHS SELECT	LS DPAD LS DPAD
DPAD5	TO-72 4L	DPAD5 TO-72 4L RoHS DPAD5 TO-72 4L RoHS SELECT	LS DPAD LS DPAD
DPAD10	TO-72 4L	DPAD10 TO-72 4L RoHS DPAD10 TO-72 4L RoHS SELECT	LS DPAD LS DPAD
DPAD20	TO-72 4L	DPAD20 TO-72 4L RoHS DPAD20 TO-72 4L RoHS SELECT	LS DPAD LS DPAD
DPAD50	TO-72 4L	DPAD50 TO-72 4L RoHS DPAD50 TO-72 4L RoHS SELECT	LS DPAD LS DPAD
DPAD100	TO-72 4L	DPAD100 TO-72 4L RoHS DPAD100 TO-72 4L RoHS SELECT	LS DPAD LS DPAD
SSTDPAD5	SOIC 8L	SSTDPAD5 SOIC 8L	LS DPAD or LS DPADF

SSTDPAD10	SOIC 8L	SSTDPAD5 SOIC 8L SELECT	LS DPAD or LS DPADF
		SSTDPAD5 SOIC 8L RoHS	LS DPADF
		SSTDPAD5 SOIC 8L RoHS SELECT	LS DPADF
		SSTDPAD10 SOIC 8L	LS DPAD or LS DPADF
		SSTDPAD10 SOIC 8L SELECT	LS DPAD or LS DPADF
		SSTDPAD10 SOIC 8L RoHS	LS DPADF
		SSTDPAD10 SOIC 8L RoHS SELECT	LS DPADF
SSTDPAD20	SOIC 8L	SSTDPAD20 SOIC 8L	LS DPAD or LS DPADF
		SSTDPAD20 SOIC 8L SELECT	LS DPAD or LS DPADF
		SSTDPAD20 SOIC 8L RoHS	LS DPADF
		SSTDPAD20 SOIC 8L RoHS SELECT	LS DPADF
SSTDPAD50	SOIC 8L	SSTDPAD50 SOIC 8L	LS DPAD or LS DPADF
		SSTDPAD50 SOIC 8L SELECT	LS DPAD or LS DPADF
		SSTDPAD50 SOIC 8L RoHS	LS DPADF
		SSTDPAD50 SOIC 8L RoHS SELECT	LS DPADF
SSTDPAD100	SOIC 8L	SSTDPAD100 SOIC 8L	LS DPAD or LS DPADF
		SSTDPAD100 SOIC 8L SELECT	LS DPAD or LS DPADF
		SSTDPAD100 SOIC 8L RoHS	LS DPADF
		SSTDPAD100 SOIC 8L RoHS SELECT	LS DPADF